

FIG. 1 (PRIOR ART)

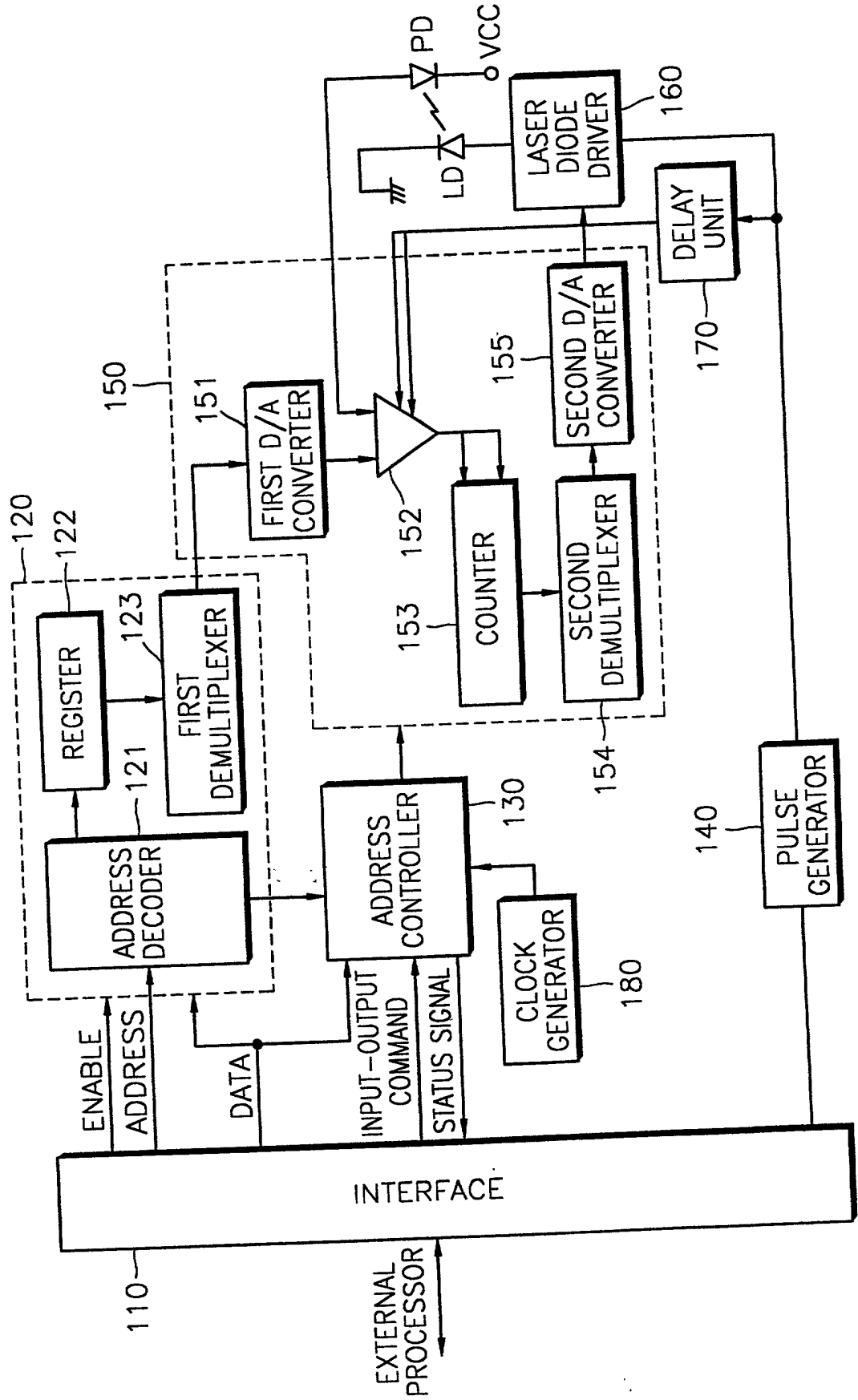


FIG. 2

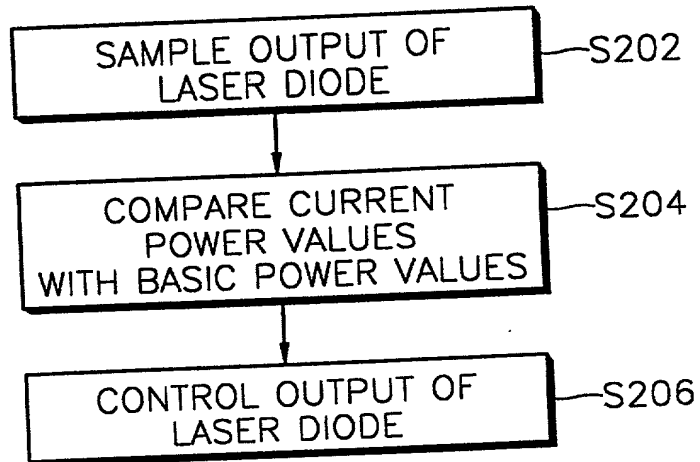


FIG. 3

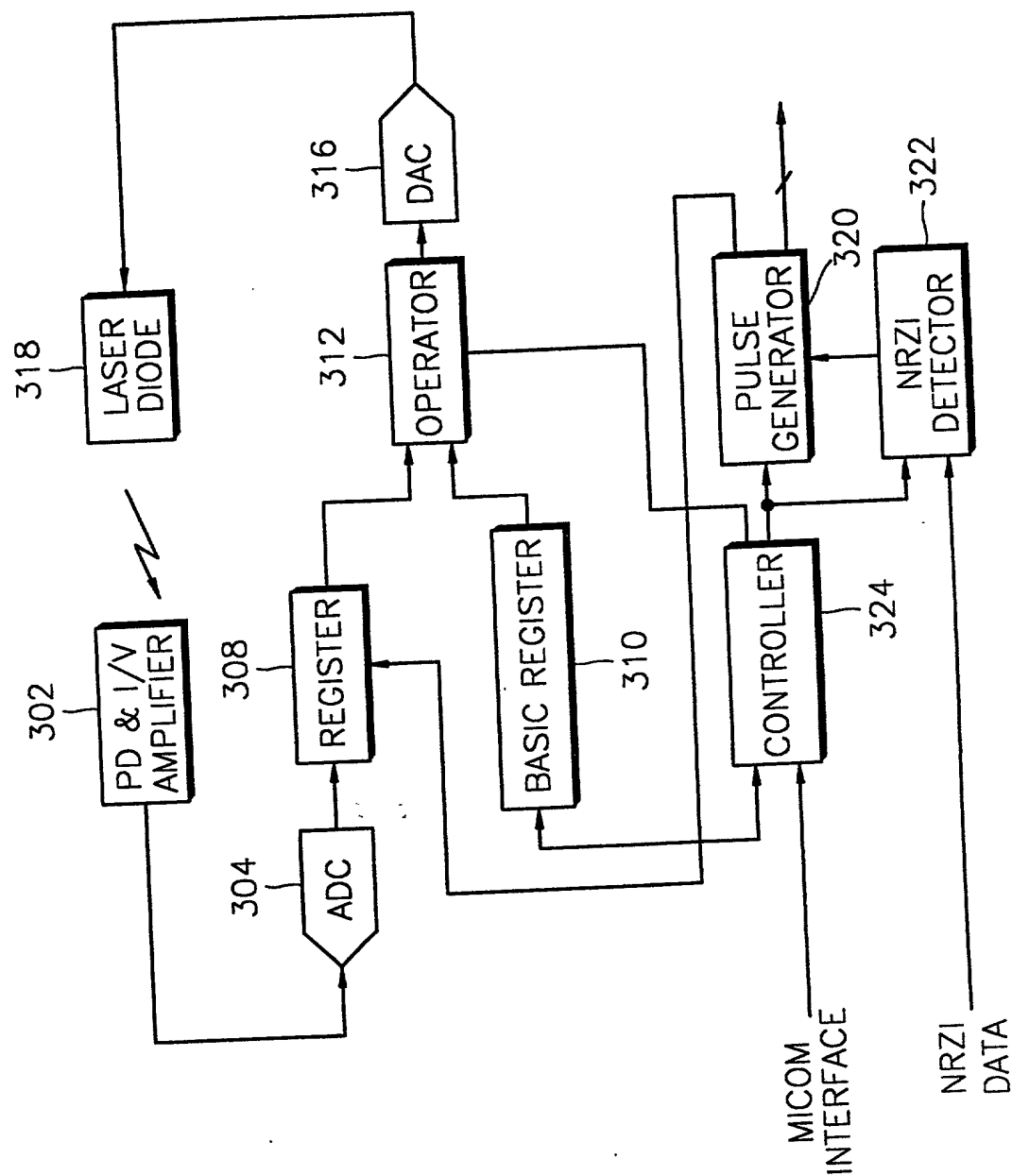


FIG. 5(a)

INPUT
NRZI DATA

FIG. 5(b)

RECORDING WAVEFORM
(LD OUTPUT SIGNAL)

FIG. 5(c)

ADC INPUT SIGNAL
(PD & I/V OUTPUT)

FIG. 5(d)

READ CONTROL SIGNAL

FIG. 5(e)

PEAK CONTROL SIGNAL
(WRITE)

FIG. 5(f)

BIAS1 CONTROL
SIGNAL (ERASE)

FIG. 5(g)

BIAS2 CONTROL
SIGNAL (COOLING)

FIG. 5(h)

BIAS3 CONTROL
SIGNAL (BOTTOM)

